

L Number	Hits	Search Text	DB	Time stamp
2	9	455/41.1.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:03
8	2	375/297.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:04
7	4	375/257.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:04
6	18	375/238.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:05
5	8	340/10.4.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:06
3	1	455/106.ccls. and (integrated adj circuit) and (capacit\$4) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:07
-	526	340/10.1.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 11:20
-	18	340/10.1.ccls. and (integrated adj circuit) and (reduc\$6 near power)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:44
-	3	340/10.1.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:50
-	32	455/\$.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:52
-	7	455/\$.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/15 10:00
-	0	375/238.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:55
-	0	375/257.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:56
-	0	375/295.ccls. and (integrated adj circuit) and (duration near4 capacitor\$2) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/13 14:56

	43	340/\$.cccls. and (integrated adj circuit) and (duration near4 capacitor\$2) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 14:59
	1	455/\$.cccls. and (integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:03
	1	235/\$.cccls. and (integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:03
	7	(integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:10
	6	(integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5) and (antenna\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:12
	8	(integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5) and (induct\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:20
	30	(integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:57
	1	((integrated adj circuit) and (duration same asynchronous\$4 same capacit\$5)) and (transponder\$2 or tag\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:40
	100	(integrated adj circuit) and (pulse same asynchronous\$4 same capacit\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 15:58
	13	(integrated adj circuit) and (pulse same asynchronous\$4 same capacit\$5) and (coil\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 16:10
	1424	clock near extract\$4 near circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/13 16:22
	33	(clock near extract\$4 near circuit) same transistor\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/14 10:01
	1636	inhibit\$4 near clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/14 10:13
	2	inhibit\$4 near clock near extract\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/14 10:02

-	6	inhibit\$4 near clock near device	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:06
-	1	(inhibit\$4 near clock) same (load near modulation near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:08
-	5	(inhibit\$4 near clock) same (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:45
-	2264	block\$4 near clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:13
-	33	(block\$4 near clock) and (modulat\$4 near pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:13
-	17	(voltage with capacitor\$2 with equal with stop\$4 with pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 10:51
-	3	(constant with current with voltage with capacitor\$2 with equal with stop\$4 with pulse\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 13:36
-	16	duration with pulse\$2 with equal with less with binary with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 13:38
-	1	duration with pulse\$2 with equal with less with quarter with binary with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 13:39
-	2	duration with pulse\$2 with equal with quarter with binary with signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/04/14 20:17